

Serial No. 10/692,483  
Amdt. dated May 21, 2007  
Reply to Office action of Dec. 22, 2006

**Amendments to the Specification:**

Please replace the paragraph from page 8, line 19 to page 8, line 28, with the following amended paragraph:

Using this method, since the input C of FF in the output side is "0", on/off of the random pulse wave P1 at the input D in FF in the input side, which is stored in the input D of the FF at the rising edge part of the clock c1 at the input C in FF in the input side, is transferred to the output Q of FF in the output side as it is. In the falling edge part of the clock c1 at the input C, the input C of FF in the output side becomes the ~~falling~~ rising edge part, so that the output Q is maintained and is not changed until next rising edge part of the clock c1 at the input C.